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PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicants : WATANABE, T., et al.
Serial No. : 08/305,528
Filed : 13 September 1994
For : SEMICONDUCTOR DEVICE INCORPORATING
INTERNAL POWER SUPPLY FOR
COMPENSATING FOR DEVIATION ON
OPERATING CONDITION AND FABRICATION
PROCESS CONDITIONS
Group Art Unit : 2511
Examiner : J. Clawson, Jr.

HONORABLE COMMISSIONER OF PATENTS
AND TRADEMARKS
Washington, D.C. 20231

SUPPLEMENTAL PRELIMINARY AMENDMENT

S I R:

Prior to examination of the above-identified Rule 60 Continuation Application,
please enter the following amendments.

IN THE CLAIMS:

Please amend claims 21, 22 and 24 as follows:

B1
B1
Claim 21 (Amended)

A semiconductor memory in a chip, comprising:

B